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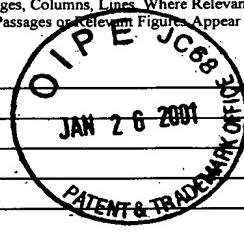
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Application Number	08/799,506
		Filing Date	February 12, 1997
		First Named Inventor	YAMAZAKI, S.
		Group Art Unit	2822
		Examiner Name	Wilczewski
Sheet		of	Attorney Docket Number
			740756-1630

## **U.S. PATENT DOCUMENTS**



## **FOREIGN PATENT DOCUMENTS**

#### **OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>MW</i>		<i>SILICON PROCESSING FOR THE VLSI ERA</i> , Vol. 1, pp. 61-65, 1994, WOLF, et al.	
<i>MW</i>		<i>SILICON PROCESSING FOR THE VLSI ERA</i> , Vol. 3, p. 648, 1995, WOLF, et al.	

Examiner Signature	M.Wilczewski	Date Considered	11/2001
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